

# EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8368	((709/203) or (709/204) or (709/209) or (709/250) or (713/401) or (713/503) or (713/600) or (713/400) or (713/401) or (713/500) or (713/503) or (710/126) or (710/300)).CCLS.	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/13 20:58
L2	0	(ALLEN and JAMES) and ((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/13 21:01
L3	51	((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:07
L4	0	(MAYFIELD and MICHAEL and JOHN) and ((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/13 21:01
L5	0	(NG and ALVAN and WING) and ((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:02
L6	36515	(memory with control\$4) same ((transmit\$4 or send\$3 or forward\$3) near5 signal)	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:04
L7	363	L1 and L6	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:05
L8	0	L6 same L3	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:05
L9	11	L6 and L3	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:05
L10	0	L9 and L1	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:05
L11	11	((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )same first same second	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:07

## EAST Search History

L12	6	((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )same (first with (microprocessor or processor or micro-processor))	USPAT; - EPO; JPO; DERWENT	OR	ON	2006/09/13 21:08
L13	34	((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )and (first with (microprocessor or processor or micro-processor))and (second with (microprocessor or processor or micro-processor))	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:08
L14	27	((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )and ((first with (microprocessor or processor or micro-processor))same (second with (microprocessor or processor or micro-processor)))	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:08
L15	3	((microprocessor or processor or micro-processor) same (bus near5 logic) same (clock\$3 near5 delay\$3) )same ((first with (microprocessor or processor or micro-processor))same (second with (microprocessor or processor or micro-processor)))	USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/13 21:08